

■ Information Technology Engineers Examination ———

Embedded Systems Specialist  
Examination  
(Level 4)  
Syllabus

— Details of Knowledge and Skills Required for  
the Information Technology Engineers Examination —

Version 3.1

**IPA**

INFORMATION-TECHNOLOGY PROMOTION AGENCY, JAPAN

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Major category	Minor category	Outline		Required knowledge	Required skills
1 Understanding of the Functional Requirements for Embedded Systems	1-1 Understanding hardware and software requirements specifications according to system functional requirements	(1) Confirmation of requirements to implement the required functions	Confirm the requirements of the embedded system, examine the hardware and software requirements specifications in detail which were created based on system functional requirements, and determine the items to include in the hardware design document and software specifications.	<ul style="list-style-type: none"> <li>• Functions, performance, and characteristics of each component device</li> <li>• Hardware design</li> <li>• Functional allocation</li> <li>• Selection of the optimal implementation means from plural options</li> <li>• Failure and exception handling</li> <li>• High-reliability system configuration method</li> </ul>	<ul style="list-style-type: none"> <li>• Appropriately allocating required functions according to the functions, performance, and characteristics of each component device</li> <li>• Appropriately allocating failure and exception handling</li> <li>• Appropriately determining the interface between component devices</li> </ul>
	1-2 Investigation of associated technologies, intellectual property rights of other companies, and related regulations	(1) General technical investigation of associated technology fields			
		(2) Trend investigation of technologies such as IoT, big data, AI, etc.			
		(3) Trend investigation of hardware and software configuration methods and element technologies			
		(4) Trend investigation of development technologies and environment, and tools			
		(5) Investigation of the technologies owned by other companies			
		(6) Consideration of the handling of technologies owned by other companies			
		(7) Investigation of product liability issues			
		(8) Investigation of safety standards			

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2 Design and Development of Embedded Systems	2-1 Determination of the interface between subsystems based on system architecture	<ol style="list-style-type: none"> <li>(1) Determination of devices to interface with external devices</li> <li>(2) Determination of the communication method between devices</li> <li>(3) Determination of the internal configuration of devices</li> <li>(4) Determination of the interface between units</li> <li>(5) Confirmation of real-time processing</li> <li>(6) Determination of process distribution</li> </ol>	<p>Consider and determine the unit of the subsystem, device, and unit (i.e. the minimum unit of a system component that possesses an interface) according to the system architecture specifications. In addition, consider and determine the interface and communication methods between these units. Confirm the real-time processing required for the system.</p>	<ul style="list-style-type: none"> <li>• Validation of hardware functions, performance, and interface</li> <li>• Hardware restrictions</li> <li>• Hardware characteristics and specifications</li> <li>• Fault tolerance</li> <li>• Functional safety design</li> <li>• Real-time processing</li> </ul>	<ul style="list-style-type: none"> <li>• Selecting appropriate hardware from the system's perspective</li> <li>• Proposing methods to achieve necessary high-reliability and safety</li> <li>• Verifying validity of the interface between the subsystems</li> </ul>
	2-2 Determination of function and performance allocation among hardware, software, and firmware	<ol style="list-style-type: none"> <li>(1) Function allocation among hardware, software, and firmware</li> <li>(2) Determination of interface specifications among hardware, software, and firmware</li> <li>(3) Selection of the microprocessor and micro controller that satisfy hardware functional requirements, and evaluation of system LSI implementation</li> </ol>	<p>Determine the optimal allocation of functions which should be implemented by the hardware of each device and unit, functions which should be processed by the software, and functions which should be processed by the firmware.</p> <p>Consider the interfaces among the hardware, software, and firmware, and clarify their interface points. Define the control signals, data, and timing so that their interpretation is not ambiguous and required information is not missing.</p> <p>Consider not only functions, but also the performance required for the system in order to determine the optimal allocation of performance between the hardware and software.</p>	<ul style="list-style-type: none"> <li>• Hardware performance, functions, and interface</li> <li>• Hardware characteristics</li> <li>• Software characteristics</li> <li>• Usage of OS, languages, and commercially-available libraries</li> <li>• Using software to get better performance out of the hardware</li> <li>• Using hardware to get better performance out of the software</li> <li>• State-of-the-art hardware and trends</li> <li>• Cost</li> <li>• Proper evaluation of characteristics of plural configuration methods</li> <li>• Possibility of long-term supply of hardware components to be used</li> </ul>	<ul style="list-style-type: none"> <li>• Determining the appropriate allocation of functions between the hardware and software, according to the restrictions required for the system</li> <li>• Reexamining the allocation of functions between the hardware and software from the viewpoint of development efficiency and implementation space</li> <li>• Reexamining the allocation of performance between the hardware and software from the viewpoint of hardware characteristics</li> </ul>

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	2-3 Verification of feasibility such as overall performance estimation and design review	(1) Feasibility verification by experiment (2) Determination of human interface by prototyping (3) Design review	Verify feasibility and human interface specifications through experiment or prototyping techniques as required. In addition, perform a design review to evaluate the feasibility from the perspective of both hardware and software designs.	<ul style="list-style-type: none"> <li>• Specific implementation methods</li> <li>• Prototyping techniques</li> <li>• Simulation techniques</li> <li>• Design review techniques</li> <li>• Implementation items and points to keep in mind in system design</li> </ul>	<ul style="list-style-type: none"> <li>• Examining the validity of the functions allocated between the hardware and software, and the validity of the interface</li> <li>• Verifying the validity of the implementation method against system requirements</li> <li>• Coordinating matters under consideration from the perspective of the entire system</li> <li>• Pointing out issues logically</li> <li>• Presenting alternative plans</li> </ul>
	2-4 Investigation of implementation functions in consideration of development period and cost	(1) Estimation of work items, workload, and period, as well as development resources and cost (2) Examination of the balance between development period, cost, and function volume	Estimate the work items, workload, period, development resources, and cost, while giving consideration to the hardware development. In addition, examine the balance between the development period, cost, and function volume (including the restrictions) to verify the validity of the estimation.	<ul style="list-style-type: none"> <li>• Development productivity of hardware and software respectively</li> <li>• Development processes of hardware and software respectively</li> <li>• Co-design</li> <li>• Resource allocation</li> <li>• Cost</li> <li>• Introduction of (critical) technologies</li> </ul>	<ul style="list-style-type: none"> <li>• Calculating development person-hours according to development productivity</li> <li>• Coordinating the hardware and software development schedule</li> <li>• Calculating the appropriate development period by considering the number of resources and cost</li> </ul>
	2-5 Work results management and determination of various recording methods	(1) Implementation of progress management and storage of progress performance records (2) Implementation of quality management and storage of quality records (3) Storage of access records to materials of other companies	Have an accurate understanding of the current position within the work plan, identify problems at an early stage, analyze them, and take countermeasures. Manage work so that they are executed in accordance with the development procedure. Also, store the various performance records.	<ul style="list-style-type: none"> <li>• Data organization and analysis techniques</li> <li>• Intellectual property rights and trade secrets</li> <li>• Record management methods</li> </ul>	<ul style="list-style-type: none"> <li>• Performing project management</li> <li>• Systematically organizing and analyzing information</li> </ul>

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	2-6 Process planning	(1) Preparation of the process plan (2) Preparation of the estimation (3) Preparation of the process management plan (4) Preparation of the training plan	Prepare a specific plan to develop the software, focusing on consistency with the hardware development plan. Also, document the completion criteria of each stage of development.	<ul style="list-style-type: none"> <li>• Process planning procedures</li> <li>• Development procedures</li> <li>• Design and development techniques</li> <li>• Estimation techniques</li> <li>• Staff training</li> <li>• Project planning and management techniques</li> <li>• Cost</li> <li>• Process deliverables</li> </ul>	<ul style="list-style-type: none"> <li>• Collecting, organizing, and analyzing estimation-related information such as specification surveys and past data</li> <li>• Coordinating with other departments to secure resources</li> <li>• Evaluating personnel skills according to development techniques and environment, and training them</li> <li>• Calculating and evaluating costs</li> <li>• Determining the development processes and the development management framework for each process</li> <li>• Coordinating with the hardware development plan</li> <li>• Managing risks</li> </ul>
	2-7 Formulation and design of policy for implementing quality requirements	(1) Formulation and design of policy for implementing quality requirements such as real-time property, functional safety, high reliability, environmental safety, security, and energy saving	Establish a required level for each of various quality requirements, and design a method for implementing the level.	<ul style="list-style-type: none"> <li>• Real-time design, functional safety design, high reliability design, environmental safety design, security design, and energy-saving design</li> <li>• Non-functional requirements</li> </ul>	<ul style="list-style-type: none"> <li>• Understanding an appropriate system--required level for each of quality requirements, such as real-time design, functional safety design, high reliability design, environmental safety design, security design, and energy-saving design, and designing an implementation means</li> </ul>
	2-8 Design of development environment	(1) Development tool environment preparation planning (2) Design of the development environment (3) Standardization planning	Prepare a specific plan to establish the development environment, based on the development status of the target hardware. Establish the software development environment. Prepare specific plans for the distributed development environment, work equipment, and testing equipment.	<ul style="list-style-type: none"> <li>• Development equipment</li> <li>• Development environment and tools</li> <li>• Purchase, rental, and lease</li> <li>• Availability of development environments on the Internet</li> <li>• Development standards</li> <li>• Reuse</li> </ul>	<ul style="list-style-type: none"> <li>• Understanding the impact of installing the development environment</li> <li>• Arranging for the development equipment and tools through appropriate channels</li> <li>• Coordinating with the training department for the installed tools</li> </ul>

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3 Design and Manufacturing of Embedded System Software	3-1 Creation of software specifications	(1) Determination of task configuration and priority of tasks to ensure real-time processing (2) Examination of inter-task communication methods and necessity of exclusive control, and determination of their implementation methods (3) Memory allocation and memory map etc. (4) Preparation of software specifications	Examine the software configurations that meet software requirements specifications, and furthermore examine the task configurations that ensure real-time processing. Determine the data communication and exclusive control methods between tasks, and document them as software specifications.	<ul style="list-style-type: none"> <li>Items which should be determined as software specifications</li> <li>Real-time OS</li> <li>Task design</li> <li>Critical section and exclusive control</li> <li>System calls used by the real-time OS</li> <li>Hardware restrictions</li> <li>Software design techniques</li> <li>Task configuration, common data, OS resources, and class design concept</li> <li>Object-oriented design and structured design</li> <li>Software characteristics</li> </ul>	<ul style="list-style-type: none"> <li>Systematically organizing and documenting the matters considered</li> <li>Extracting real-time requirements from the target system requirements</li> <li>Designing task configurations that satisfy functional requirements</li> <li>Designing the optimum communication method between tasks</li> <li>Evaluating the validity of preemption from the relationship between interrupts and real-time processing</li> <li>Examining the existence of critical sections, and designing optimal exclusive control</li> <li>Making use of necessary system calls</li> <li>Arranging the requirements for hardware from the viewpoint of software</li> <li>Considering measures in response to exceptions, errors, and hardware failures</li> </ul>
	3-2 Design of device driver	(1) Design a device driver for dedicated hardware	Make a device driver for hardware as necessary when no device driver is available.	<ul style="list-style-type: none"> <li>Real-time OS</li> <li>Device driver</li> </ul>	<ul style="list-style-type: none"> <li>Understanding interfaces required for hardware operations</li> <li>Designing device drivers</li> </ul>
	3-3 Review at each stage of the software life cycle	(1) Review planning (2) Validity judgment of deliverables from each stage and issues pointed out (3) Response to issues pointed out in the review (4) Judgment of validity in moving to the next stage	Review the work at each stage in software development based on the performance records and deliverables to ensure that it satisfies the requirements.  Evaluate the review results and the validity of the response to issues pointed out in the review, and judge whether or not the development project can proceed to the next stage.	<ul style="list-style-type: none"> <li>Software life cycle process</li> <li>Implementation items and points to keep in mind in system design</li> <li>Validity judgment based on software deliverables</li> <li>Industry and products</li> <li>Review techniques</li> <li>Functional safety design</li> </ul>	<ul style="list-style-type: none"> <li>Determining the required development level according to system functional requirements</li> <li>Presenting alternative plans</li> <li>Presenting an optimal plan from a comprehensive viewpoint</li> <li>Evaluating the design quality</li> <li>Evaluating the development process based on the progress results and quality records</li> </ul>

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	3-4 Creation of the program and extraction of program test items	<ol style="list-style-type: none"> <li>(1) Determination of the standard source program description format</li> <li>(2) Preparation of the program creation environment</li> <li>(3) Program creation</li> <li>(4) Selection of test methods</li> <li>(5) Extraction of program unit test items</li> <li>(6) Extraction of program test items</li> <li>(7) Determination of fault isolation procedures</li> </ol>	<p>Determine the standard description format for the source program in order to create programs. Also, select test methods which satisfy the quality level required for the program, and extract the test items to be executed.</p>	<ul style="list-style-type: none"> <li>• Programming techniques</li> <li>• Programming efficiency</li> <li>• Program quality</li> <li>• Program test techniques</li> <li>• Program test procedures</li> <li>• Program test environment</li> </ul>	<ul style="list-style-type: none"> <li>• Clarifying programming guidelines according to software specifications</li> <li>• Introducing measures to improve programming efficiency and ensure quality</li> <li>• Extracting and systematically organizing program test items</li> <li>• Determining the program test environment</li> <li>• Determining the program test order</li> </ul>
	3-5 Construction of the test environment	<ol style="list-style-type: none"> <li>(1) Preparation of test equipment</li> <li>(2) Securing the test location</li> <li>(3) Securing test staff</li> </ol>	<p>Confirm the number of debugging tools, test staff, and the location to deploy the system, and build the test environment.</p>	<ul style="list-style-type: none"> <li>• Integration test equipment</li> <li>• Integration test environment and tools</li> <li>• Purchase, rental, and lease</li> <li>• Connection of the integration test equipment</li> <li>• Integration test process</li> </ul>	<ul style="list-style-type: none"> <li>• Arranging for the test equipment and tools through appropriate channels</li> <li>• Cooperating with the test tool training department</li> <li>• Connecting the test equipment to build the test environment</li> </ul>
	3-6 Test execution	<ol style="list-style-type: none"> <li>(1) Construction and maintenance of the test environment</li> <li>(2) Test execution</li> <li>(3) Negotiation with other departments</li> <li>(4) Handling of unexpected events</li> <li>(5) Fault isolation</li> <li>(6) Confirmation test with the final version</li> </ol>	<p>Execute the test according to the test specifications. Plan and implement measures in response to failures that have occurred during the test.</p>	<ul style="list-style-type: none"> <li>• Points to confirm in the integration test</li> <li>• Test equipment</li> <li>• Test techniques</li> <li>• Test procedures</li> <li>• Test environment</li> <li>• Measures in response to failures</li> <li>• Quality evaluation and forecasting methods</li> <li>• Analysis of the situation in the event of failure</li> <li>• Utilization methods of quality data</li> <li>• Writing presentation skills</li> </ul>	<ul style="list-style-type: none"> <li>• Teaching test methods including test equipment usage</li> <li>• Analyzing the cause of a failure (hardware and software isolation)</li> <li>• Collecting and analyzing the situation in the event of failure</li> <li>• Reconsidering the test items in response to the situation of failures or the content of the failures</li> <li>• Managing the situation in the event of failure</li> <li>• Collaborating with the person in charge of the hardware</li> <li>• Coordinating with other departments</li> <li>• Analyzing the content and situation of failures, and evaluating quality</li> <li>• Analyzing the quality situation</li> </ul>



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	3-7 Integration test of hardware and software (system verification test)	<ol style="list-style-type: none"> <li>(1) Determination of the integration test commencement period</li> <li>(2) Preparation of a test location plan</li> <li>(3) Creation of a test preparation plan</li> <li>(4) Preparation of a personnel plan</li> <li>(5) Execution of the integration test</li> </ol>	<p>Define the conditions to execute the hardware and software integration test, and prepare a specific plan to execute the integration test.</p> <p>Also, clearly state the responsibilities assigned to each group.</p> <p>Execute the integration test.</p> <p>Plan (i.e. determine the date, reviewers, and location) and conduct a review of the deliverables and performance records from the hardware and software integration test.</p> <p>Take measures in response to issues pointed out in the review.</p>	<ul style="list-style-type: none"> <li>• Hardware and software development processes</li> <li>• Purpose and confirmation items of the integration test</li> <li>• Integration test methods, tools, and equipment</li> <li>• Productivity in test execution</li> <li>• Evaluation criteria</li> <li>• Review techniques</li> </ul>	<ul style="list-style-type: none"> <li>• Presenting policies for the test environment, including the purpose, method, and completion criteria of the test</li> <li>• Coordinating the process plan with hardware development</li> <li>• Extrapolating necessary equipment from the test plan, and making arrangements to procure them</li> <li>• Extracting items to be executed in the integration test, and organizing them systematically</li> <li>• Evaluating system quality</li> <li>• Evaluating the development process based on the progress results and quality records</li> </ul>
	3-8 Configuration management	<ol style="list-style-type: none"> <li>(1) Extraction of the configuration management target document</li> <li>(2) Preparation of rules for administering configuration management</li> </ol>	<p>Prepare specific plans and rules to implement configuration management.</p>	<ul style="list-style-type: none"> <li>• Purpose and method of configuration management and points to keep in mind</li> <li>• Configuration management tools</li> </ul>	<ul style="list-style-type: none"> <li>• Selecting the appropriate configuration management tools for the system</li> </ul>
	3-9 Review of program creation and program test	<ol style="list-style-type: none"> <li>(1) Review planning</li> <li>(2) Reviewing</li> <li>(3) Response to issues pointed out in the review</li> <li>(4) Completion criteria for program creation work</li> <li>(5) Completion criteria for program test work</li> </ol>	<p>Plan (i.e. determine the date, reviewers, and location) and conduct a review of the deliverables and performance records from the program creation and program test, and respond to issues pointed out in the review. In addition, judge whether program creation and program test are complete based on the review results.</p>	<ul style="list-style-type: none"> <li>• Execution items and points to keep in mind in program creation and program test</li> <li>• Development processes</li> <li>• Evaluation criteria</li> <li>• Review techniques</li> </ul>	<ul style="list-style-type: none"> <li>• Addressing issues logically</li> <li>• Presenting alternative plans</li> <li>• Presenting an optimal plan from a comprehensive viewpoint</li> <li>• Evaluating program quality</li> <li>• Evaluating the development process based on the progress results and quality records</li> </ul>

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	3-10 Work results management and records	(1) Implementation of progress management and storage of progress performance records (2) Implementation of quality management and storage of quality records (3) Management of the program change history (4) Storage of records relating to the root cause of failures and measures	Have an accurate understanding of the current position within the work plan, identify problems at an early stage, analyze them, and take countermeasures. When doing so, keep records of specification changes, occurrences of failure, and measures, as well as the minutes of general progress meetings.	<ul style="list-style-type: none"> <li>Development management target data</li> <li>Data organization and analysis techniques</li> <li>Record management methods</li> <li>Version control</li> </ul>	<ul style="list-style-type: none"> <li>Performing project management</li> <li>Systematically organizing and analyzing information</li> <li>Understanding the software components and managing the history</li> </ul>
4 Design and Manufacturing of Embedded System Hardware	4-1 Hardware design document	(1) Analysis of hardware requirements specifications (2) Determination of the hardware architecture (3) Design of the interfaces between subsystems and units (4) Determination of the I/O method (5) Safety and reliability design	Consider and determine the hardware architecture to satisfy the safety and reliability in accordance with the system functional requirements specifications. Examine the hardware requirements specifications and determine the appropriate hardware configuration. Determine the interface between subsystems and units. Determine the I/O methods for the system and the rated I/O performance. Organize them into a hardware design document.	<ul style="list-style-type: none"> <li>Reliability engineering</li> <li>Fault tolerant systems</li> <li>Fail-safe design</li> <li>Multi-processor systems</li> <li>Serial communications</li> <li>Serial interface standards</li> <li>I/O circuits</li> <li>Timer functions</li> <li>Analog signal processing</li> </ul>	<ul style="list-style-type: none"> <li>Designing a system which satisfies reliability and safety requirements</li> <li>Evaluating system reliability</li> </ul>

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	4-2 Design of processing units	(1) Selection of the MPU, MCU, and DSP (2) Consideration of system LSI (3) Memory hierarchy (4) Examination of the internal bus structure (5) Use of peripheral LSI (6) Use of programmable LSI (7) Power-saving design (8) Thermal design	Select an MPU, MCU, and DSP which satisfies the performance requirements for the unit. Design the memory hierarchy in accordance with the cost and processing performance. Design an optimal internal bus considering data movement between internal structure elements. Judge whether system LSI is appropriate and whether a programmable LSI is available considering space-saving benefit and future product development. Make proper use of peripheral LSIs. Design the circuit considering power-saving and thermal design.	<ul style="list-style-type: none"> <li>MPU, MCU, DSP, and peripheral LSI</li> <li>Audio and imaging devices</li> <li>Codec</li> <li>System LSI</li> <li>Programmable LSI</li> <li>Memory hierarchy</li> <li>Speeding up of MPUs</li> <li>High-level hardware description languages</li> <li>Power-saving design</li> <li>Heat radiation and thermal design</li> </ul>	<ul style="list-style-type: none"> <li>Appropriately selecting and using MPU, MCU, DSP, and peripheral LSI</li> <li>Making proper use of audio and imaging devices</li> <li>Fully utilizing codec to improve quality and reduce costs</li> <li>Judging whether to implement the circuit components as a system LSI or a programmable LSI</li> <li>Understanding system LSI design using high-level hardware description languages, and judging feasibility</li> <li>Designing a memory hierarchy to realize higher speed and lower cost</li> <li>Understanding and using methods to make a faster MPU to improve processing efficiency</li> </ul>

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	4-3 Design of interfaces to the software	(1) Interrupts (2) I/O table (3) Interface for the power-saving mechanism (4) Communication protocol (5) Human interface	Appropriately allocate interrupts required by the system according to priority level, and link them with the software interrupt processing program. Determine the I/O table specifications for interfacing with the system I/O and peripheral LSI. Design the interface requirements with the software according to the transmission specifications. Determine the specifications to utilize the power-saving mechanism. Select and use a communication protocol suitable for the requirements specifications, and evaluate the performance. Propose an appropriate human interface, and determine the software specifications.	<ul style="list-style-type: none"> <li>Interrupt control</li> <li>I/O methods</li> <li>Design of the communication protocol</li> <li>Evaluation of the transmission rate</li> <li>Power-saving technologies</li> <li>Human interface</li> </ul>	<ul style="list-style-type: none"> <li>Designing an interrupt control circuit</li> <li>Selecting and designing an appropriate I/O method</li> <li>Designing a communication protocol suitable for the required specifications, and evaluating the performance</li> <li>Designing the system utilizing power-saving technologies</li> <li>Proposing an appropriate human interface based on system functional specifications</li> </ul>
	4-4 Design of the I/O interface	(1) A/D and D/A (2) USB (3) Wired/wireless communication interface (4) Flash memory (5) Auxiliary storage devices	Design a circuit to obtain physical quantities using an A/D or D/A conversion circuit and to drive actuators. Make proper use of various devices with an USB interface. Make effective use of wired/wireless communication interface. Make effective use of flash memory. Make effective use of auxiliary storage devices.	<ul style="list-style-type: none"> <li>A/D and D/A conversion circuits</li> <li>Various devices using an USB interface</li> <li>Wired/wireless communication interface</li> <li>Flash memory</li> <li>Auxiliary storage devices</li> </ul>	<ul style="list-style-type: none"> <li>Designing a circuit to obtain physical quantities using an A/D or D/A conversion circuit and to drive actuators</li> <li>Making proper use of wired/wireless communication interface</li> <li>Making proper use of USB interface products and flash memory</li> <li>Using auxiliary storage devices based on performance conditions</li> </ul>

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	4-5 Manufacturing of hardware	(1) System verification test (2) Preparation of the development environment and test environment (3) EMC evaluation (4) Issues concerning electricity and machinery	Use test tools to test the manufactured hardware to check whether or not it satisfies the requirements specifications. Execute an integration test with the software to ensure that the product satisfies the functional requirements. Prepare the development and test environments used for manufacturing and testing. Confirm that the EMC-related standard is satisfied. Evaluate the tolerance toward disturbances in the electrical and mechanical environment such as voltage fluctuation and vibration, and take measures if necessary.	<ul style="list-style-type: none"> <li>• Hardware unit test</li> <li>• Integration test with the software</li> <li>• Product development environment and test environment</li> <li>• EMC standard</li> <li>• EMC test</li> <li>• Disturbances around electrical and mechanical systems such as voltage fluctuation and vibration, and countermeasures</li> <li>• Occurrence mechanism of latch-up and its countermeasures</li> </ul>	<ul style="list-style-type: none"> <li>• Conducting the hardware unit test</li> <li>• Preparing and executing the integration test with the software, and judging the test results</li> <li>• Preparing the development and test environments used for manufacturing and testing</li> <li>• Executing and evaluating the EMC test</li> <li>• Executing the environmental disturbance test around electrical and mechanical systems and taking countermeasures</li> </ul>
5 System Evaluation	5-1 Preparation of an inspection plan	(1) Preparation of an inspection plan	Create a specific plan to prepare for the inspection (arrangement of deliverables and management results).	<ul style="list-style-type: none"> <li>• Purpose of inspection</li> <li>• Inspection procedures</li> <li>• Deliverables and management work results necessary for inspection</li> <li>• Estimation techniques</li> </ul>	<ul style="list-style-type: none"> <li>• Extracting and systematically organizing the work items necessary for inspection, based on the organization guidelines for the deliverables and management work results</li> <li>• Determining the division of roles and work procedures</li> <li>• Calculating a valid estimate according to the work details, period, cost, and resources</li> <li>• Coordinating with other departments over the timing of inspections</li> </ul>

Major category	Minor category	Outline	Required knowledge	Required skills	
	5-2 System evaluation	<ol style="list-style-type: none"> <li>(1) Analysis of the inspection results and countermeasures</li> <li>(2) Determination of system evaluation items and scale</li> <li>(3) Performing system evaluation</li> </ol>	<p>Evaluate the developed system to confirm that it provides functions initially defined in the specifications, and evaluate the manuals supplied with the system, the achievement of technical goals, intellectual property rights, and quality objectives.</p>	<ul style="list-style-type: none"> <li>• Purpose of system evaluation</li> <li>• System evaluation framework and items</li> <li>• System evaluation criteria and evaluation methods</li> <li>• Problem analysis and solution techniques</li> <li>• System industry and products</li> <li>• Quality management and evaluation</li> <li>• Record management methods</li> </ul>	<ul style="list-style-type: none"> <li>• Evaluating consistency between the system functional requirements and customer demands</li> <li>• Managing the evaluation results as records</li> </ul>
6 Maintenance	6-1 Preparation of a maintenance plan	<ol style="list-style-type: none"> <li>(1) Preparation of a maintenance plan</li> </ol>	<p>Based on the development plan, prepare a specific plan to perform maintenance.</p> <p>Prepare a specific plan by extracting and considering the work items and work details according to the maintenance details (functional changes and responding to complaints).</p>	<ul style="list-style-type: none"> <li>• Purpose of maintenance</li> <li>• System industry</li> <li>• Types of maintenance work</li> <li>• Response methods according to the purpose of maintenance</li> <li>• Product lifecycle</li> </ul>	<ul style="list-style-type: none"> <li>• Extracting specific maintenance items according to the definition of maintenance work (the corresponding work), and organizing them systematically</li> <li>• Clarifying the scope of impact caused by the maintenance work</li> <li>• Determining the division of roles, work procedures, and maintenance framework</li> <li>• Calculating a valid estimate according to the work details, period, cost, and resources</li> </ul>
	6-2 Organization of development information for maintenance	<ol style="list-style-type: none"> <li>(1) Organization of specification documents</li> <li>(2) Organization of design documents</li> <li>(3) Storage of the program</li> <li>(4) Storage of development records</li> <li>(5) Organization of materials for the operation of the development environment</li> <li>(6) Storage of revision records</li> </ol>	<p>Organize and store documents necessary for maintenance.</p>	<ul style="list-style-type: none"> <li>• Document management method and procedures</li> <li>• Change management</li> </ul>	<ul style="list-style-type: none"> <li>• Systematically organizing and managing information for maintenance</li> <li>• Clarifying procedures to change maintenance information</li> </ul>

Major category	Minor category	Outline	Required knowledge	Required skills	
	6-3 Preparation of the maintenance environment	(1) Preparation of a version control system (2) Preparation of a customer information management system (3) Preparation of the maintenance environment	Build an environment necessary for maintenance (version control system, customer information management system, maintenance development environment, etc.).	<ul style="list-style-type: none"> <li>Version control methods</li> <li>Customer information management methods</li> <li>Maintenance of the development environment</li> </ul>	<ul style="list-style-type: none"> <li>Considering and determining version control procedures</li> <li>Considering and determining customer management procedures</li> <li>Considering the method to maintain the development environment to ensure recovery after maintenance</li> </ul>
	6-4 Implementation of maintenance	(1) Instructions for the maintenance work (2) Impact analysis and implementation of maintenance work (failure correction and functional improvement) (3) Implementation of change management and complaint management (4) Reflection in the next project	Confirm that work for failure correction and functional improvement does not deteriorate reliability, and perform the work. Record and make use of the details of changes and complaints. Furthermore, analyze the complaints, and reflect the results in the next project.	<ul style="list-style-type: none"> <li>Specific implementation items according to type of maintenance work</li> <li>Update procedures</li> <li>Analysis of impact due to software function change</li> <li>Writing presentation skills</li> </ul>	<ul style="list-style-type: none"> <li>Analyzing a failure, ascertaining the causes, and determining necessary maintenance details</li> <li>Evaluating the quality of the determined contents of maintenance</li> <li>Building a necessary framework according to the maintenance details, and taking appropriate measures</li> <li>Coordinating with other departments</li> <li>Considering and implementing emergency measures</li> <li>Determining the scope of impact caused by implementing maintenance, and taking appropriate measures</li> </ul>

**■ Embedded Systems Specialist Examination (Level 4)  
Syllabus (Version 3.1)**

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