

1st STAMP WORKSHOP IN JAPAN [Program]

Theme	STAMP WORKSHOP IN JAPAN	
Date	December 5-7, 2016	
Venue	December 5, 2016	Innamori Center, Kyusyu University
	December 6-7, 2016	West New Plaza, Kyusyu University
Symposium of Modeling and verification of social technology system		
December 5, 2016	9:00 am	Greeting from the organizer
	-9:10 am	Keijiro Araki, Chair, Kyusyu University
	9:10 am	Keynote speech : 『STAMP/STPA Beginner Introduction』
	-10:10 am	John Thomas, MIT
	10:20 am	Keynote speech : 『STAMP/STPA Intermediate Tutorial』
	-11:50 am	John Thomas, MIT
	12:00 pm	Keynote speech : 『STPA Applied to Automotive Automated Parking Assist』
	-1:00 pm	John Thomas, MIT
	1:00 pm	Intermission
2:00 pm	Introduction:	
-2:10 pm	Keijiro Araki, Chair, Kyusyu University	
2:10 pm	Invited lecture (45 minutes each including Q & A)	
-5:20 pm	Chairperson: Katsutoshi Shintani	
	『Software IV&V and STAMP』	
	Nobuyuki Hoshino, Japan Manned Space Systems Corp. (JAMSS)	
	『Research Issues for integrating System Theory and Assurance Case』	
	Shuichiro Yamamoto, Nagoya University	
6:00 pm	A social gathering	

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December 6, 2016	9:30 am -9:40 am	Message from the program chairperson Shigeru Kusakabe, Chair , University of Nagasaki
	9:40 am -11:10 am	Invited lecture (45 minutes each including Q & A) Chairperson: Shigeru Kusakabe 『Safety of Socio-Technical Systems and Resilience Engineering』 Masaharu Kitamura, Research Institute for Technology Management Strategy (TeMS) 『STAMP and FRAM』 Hideki Nomoto, Japan Manned Space Systems Corp. (JAMSS)
	11:20 am -12:00 pm	STAMP Short session (20 minutes each) Chairperson: Hideki Nomoto (1) STAMP modeling of greenhouse control Daisaku Arita, University of Nagasaki /Shigeru Kusakabe, University of Nagasaki (2) Trial to apply STAMP/STPA-sec for mitigating damage of an intranet from targeted attacks Masahiko Kato, University of Nagasaki /Shigeru Kusakabe, University of Nagasaki
1:00 pm -2:30 pm	General lecture session A (30 minutes each) Chairperson: Yukihiro Mihara (3) Safety thinking for complex system design based on STAMP/STPA Shigeru Kanemoto, School of Computer Science and Engineering, University of Aizu (4) A Trial of STAMP/STPA in ET Robot Contest and Design and Implementation of Web-based STPA tool Junro Abe*, Yuya Furukawa*, Keishi Okamoto**, Yutaka Matsuno* *Department of Computer Engineering, College of Science and Technology, Nihon University ** National Institute of Technology, Sendai College	

		<p>(5) A case study on applying STAMP/STPA to a two-wheel-pendulum of ET Robot Contest</p> <p>Fuga Ohtomo*, Yutaro Kikuchi*, Katsuaki Rikitake*, Keishi Okamoto*</p> <p>*National Institute of Technology, Sendai College</p>
	<p>2:40 pm -4:10 pm</p>	<p>General lecture session B (30 minutes each)</p> <p>Chairperson: Shogo Ishii</p> <p>(6) Safety Analysis of Personal Vehicle on Water MINAMO</p> <p>Naoyuki Takesue*, Shunnosuke Naruoka*, Yuta Otsuka*</p> <p>*Tokyo Metropolitan University</p> <p>(7) Safety Analysis of Aquatic Segway, “MINAMO”, using STAMP/STPA</p> <p>Masanori Seino*, Shigeru Kanemoto*</p> <p>*School of Computer Science and Engineering, University of Aizu</p> <p>(8) Examination of STAMP/STPA for health support system using ID card size sensor</p> <p>Ryousuke Kobayashi, Graduate School of Aichi Institute of Technology</p> <p>Nobuyuki Ito, Mitsubishi Electric Engineering</p> <p>Katsuhiko Naito*, Tadanori Mizuno*, Naoya Chujo*</p> <p>*Aichi Institute of Technology</p>
	<p>4:20 pm -5:50 pm</p>	<p>General lecture session C (30 minutes each)</p> <p>Chairperson: Keishi Okamoto</p> <p>(9) STPA for human-machine interaction system using two-wheel-pendulum</p> <p>Chiharu Sasaki, Software Software Reliability Enhancement Center (SEC), Information-technology Promotion Agency, Japan (IPA)</p> <p>(10) A Study of Applying STAMP/STPA to an Enterprise System</p> <p>Akira Mukaiyama, NEC</p> <p>(11) Trial which applied STAMP to software fault of embedded system different from control system</p> <p>Yutaka Hada, Quality Promotion Division Software Engineering Center, NEC Communication Systems, Ltd.</p>
December 7, 2016	<p>9:00 am -10:30 am</p>	<p>General lecture session D (30 minutes each)</p> <p>Chairperson: Takashi Kawano</p>

	<p>(12) STAMP / STPA analysis on functional specification of Level Crossing Control in the Station Logic Control Device</p> <p>Rihito Aman ,R&D Center of JR East Group Advanced Railway System Development Center</p> <p>(13) A proposal for "hint words" to identify hazard causal factors for systems including human</p> <p>Yukihiro Mihara, Software Reliability Enhancement Center (SEC), Information-technology Promotion Agency, Japan (IPA)</p> <p>(14) Quantitative assessment by means of STAMP and how to use STAMP in product development processes</p> <p>Tetsuya Takata ,Development Center Kyosan Electric Mfg.Co.,LTD.</p>
<p>10:40 am</p> <p>-11:40 am</p>	<p>General lecture session E (30 minutes each)</p> <p>Chairperson: Shigeru Kanemoto</p> <p>(15) Extending STPA trial for identifying UCA</p> <p>Yuko Fukushima ,Technology Research & Innovation Nihon Unisys, Ltd.</p> <p>(16) Verification Using Model Checking Technique in STAMP/STPA</p> <p>Yoshitaka Aoki ,Technology Research & Innovation Nihon Unisys, Ltd.</p>
<p>11:40 am</p> <p>-12:00 pm</p>	<p>Closing</p> <p>Moderator : Shigeru Kanemoto</p>